The Road Ahead: Outlook for the Industry

E. Jan Vardaman, Founder and President

- Track Innovation
- Identify Trends
- Analyze Growth
- Influence Decisions

Relevant, Accurate, Timely

TechSearch International
Industry Trends

- Traditional unit growth markets slowing…
  - PC sales declining (additional sales gated by Intel issues)
  - Smartphones low growth, but slight improvement for year end totals
  - Shift in interconnect from WB to FC for DRAM, especially in server market continues
  - DRAM expansion slowing but transition to FC continues
- Cryptocurrency drove unit volume growth end of 2017, but lower growth this year
- AI and HPC drives high-performance packaging
  - Driving Si interposer and FO on substrate
  - Drives HBM
  - High $ value, but low unit volumes
- Increased electronic content in automobiles with ADAS and growth in EV
CAPEX Spending Will Be Lower than Projected


- Global growth projections lower from 3.9% to 3.7% for next year
- CAPEX spending will slow to lower than projected ($22.6 billion in 2018 vs. $24.2 billion in 2017)
  - Samsung has announced it will reduce spending for DRAM, considers DRAM at peak
Global Smartphone Shipment Forecast

- **IDC expects global smartphone shipments to decline 0.7% from 2017 to 2018**
  - Positive growth for second half of 2018
- **Growth increasing in 2019**
  - Drives volumes, including SiP

Source: IDC
Bitcoin Price Trend: Party Over!!

Source: Coindesk.

- **Contributed to strong industry growth in 2017**
  - Last year ~750 million units (flip chip BGA and CSP), contributed to capacitor shortage
  - Demand cooled off
Growth in Server Market

- Demand for datacenters from Alibaba Group Holding (China), Amazon, Apple, Google, Microsoft, Facebook, and Tencent Holdings (China) to run retail operations, search engines, cloud services, and social networks over the Internet
- Demand for high-performance chips for data crunching drives demand for advanced packaging (flip chip packages)
  - Intel Xeon server processors for example (high dollar value)
  - Intel accounts for ~95% of server market, but AMD is gaining ground!
- Demand for data storage driving growth in Flash memory (SSD)
- Demand for DRAM in DIMMs
Datacenter growth continues
  - Last year Amazon, Google, Facebook, and Microsoft spent $40 billion on new plants and equipment

Drives demand for fast DRAM for servers and flash memory for storage

Source: Macquarie Capital.
Samsung’s DDR4 with TSV

- **Samsung’s 128GB RDIMM uses DDR4 memory with TSVs**
- **Targeted for datacenters**
  - Lower power
  - Double capacity of originally 64GB LRDIMM developed for Enterprise servers
What is Artificial Intelligence (AI)?

- **Artificial Intelligence**: Theory and development of computer systems able to perform tasks that normally require human intelligence
  - Visual perception and pattern recognition
  - Speech recognition
  - Decision-making
  - Natural language processing and translation

- **Machine Learning**: Branch of AI in which computers learn from data without human assistance

- **Deep Learning**: Type of machine learning that trains a computer to perform human-like tasks
  - Recognizing speech, identifying images, or making predictions
  - Sets up the parameters about the data and trains the computer to learn on its own by recognizing patterns using many layers of processing

Source: IBM.
AI Chips See Increased Demand

- TSMC reports that more than 300 million smartphones shipped with built-in AI (refers to speech recognition etc.)
- Silicon content in smartphones expanding with adoption of facial recognition and new applications such as AR, VR, and 3D video
- Increased computing for automotive ADAS will use AI in future
- AI accelerators in datacenters to increase from 15% today to 50% by 2020
  - TSMC reports a 4X growth in AI accelerators since 2016

Source: NVIDIA.

Source: Intel.
Where Do We Find AI and What Package Types are Required?

- **Accelerators for datacenters**
  - Google (ASIC + HBM2 on Si interposer)
  - NVIDIA (GPU + HBM2 on Si interposer)
  - Intel Nervana (ASIC + HBM2 on Si interposer)
  - Intel Stratix 10 (FPGA + HBM2 on EMIB)
  - Xilinx UltraScale+ (FPGA slices + HBM2 on Si interposer)
  - Baidu (ASIC + HBM2 on Si interposer)

- **Automotive autonomous driving**
  - NVIDIA DRIVE PX Pegasus (4 GPUs packaged in FC-BGAs on a board)

- **Smartphones processors with AI features**
  - Apple A11 Bionic (InFO FO-WLP)
  - Huawei Kirin 970 (flip chip in MCeP PoP)
  - Samsung Exynos 9810 (flip chip on 1-2-1 SLP substrate with embedded passives, PoP)
  - Qualcomm Snapdragon 845 (flip chip in MCeP PoP)
Google Tensor Processing Unit v2 with Interposer and HBM

- Google-designed device for neural net training and inference
  - 16 GB of HBM
  - 600 GB/s memory bandwidth
- ASIC + HBM on Si interposer using TSMC’s CoWoS

- Version 3, introduced in May 2018, doubles the HBM
Xilinx VIRTEX UltraScale+™

- VIRTEX UltraScale+™ silicon interposer with TSVs using TSMC’s CoWoS
  - Interposer as large as 30 mm x 36 mm
  - Metal line stitching used for larger than reticle interposer products
  - 3 Cu metal layers plus 1 Al layer
  - <1μm lines and spaces
  - Thickness of 100 μm
- Approximately 660,000 interconnects in the module

Source: Xilinx.
Samsung Foundry Business Seeing AI Customers from China

- **Samsung picks up AI chip design business from China design houses**
  - Baidu’s Kunlun AI accelerator for datacenters
  - Fabricated at Samsung on 14nm semiconductor node

- **ASIC and HBM2 on a Si interposer**
  - Driving Si interposer and HBM expansion
  - Driving interposer assembly

Source: Baidu.
Inte’s Silicon Bridge

- **Embedded Multi-die Interconnect Bridge (EMIB)** A small silicon bridge chip is embedded into the package (no TSVs)
  - Package substrate provided by substrate supplier (does Si bridge embedding)

- **Considered less expensive because only small area for high-density silicon and no TSVs**

- **EMIB allows the die I/O or bumps to be placed as close as possible to the edge of the die because fewer I/O or bumps are required**
  - Micro bumps on chips, communication between chips through interposer

- **Good electrical performance is reported due to the short interconnects**

Source: Intel.
FO-WLP on Substrate: Network Switch, Potential AI

- **Amkor’s Silicon Wafer Integrated Fan-out Technology (SWIFT®)**
  - RDL with 2/2μm L/S
  - Up to 3 RDLs plus UBM

- **ASE’s Fan-Out Chip on Substrate (FOCoS)**
  - RDL with 2μm/2.5 L/S
  - Up to 3 RDLs plus UBM
  - High I/O (>1,000)
  - In production since 2016

- **TSMC Integrated Fan-Out WLP on Substrate (InFO_oS)**
  - RDL with 2μm L/S
  - Up to 3 RDLs plus UBM
  - In production for MediaTek switch (split die)
Automotive Semiconductor Content Growth due to ADAS

Average ADAS* semiconductor content per level of automation

Level 2: 2015-2020 (up to 30 million vehicles per year)
Level 3: 2020-2025 (up to 10 million vehicles per year)
Level 4: 2025-2030 up to 5 million vehicles per year

In addition to current $300/car average

Source: Infineon.

 Autonomous Vehicle Platform

Autonomous vehicle platform: a functional diagram

- Sense
  - Raw data
    - GPS
    - IMS
    - Cameras
    - Radars
    - 3D Scanning Lidars
    - Ultrasound sensors
  - Sensor Processing
  - Sensor Fusion

- Understand
  - Object parameters
    - Time stamp
    - Dimensions
    - Position/Velocity
    - V2V / V2I comm.
    - "Maps" a priori info

- Act
  - Actions
    - Do nothing
    - Warn
    - Complement
    - Control
  - Driver state
  - Action Engine
  - Vehicle Controls
    - Brake/acc
    - Steering
    - etc.
  - Visualization/Display Sub-system

Compressed data
NVIDIA’s AI Platform for Autonomous Driving

- NVIDIA’s AI solutions for autonomous driving include “Pegasus”
  - GPU packaged in FC-PBGAs
  - Package size up to 42.5 mm x 42.5 mm
- NVIDIA® HGX-2™ fuses AI and high-performance computing into a single platform
- Thermal performance important
Market for Wearables

- IDC expects increase of 6.2% for wearables market this year to 122.6 million units
  - CAGR of 11.6% for 5-year forecast
- Wearables include
  - Watch (largest segment)
  - Wristband (second largest segment)
  - Earwear
  - Clothing
- Small market compared to smartphones, but growing market
Migration to FO-WLP

FC-CSP

Fan-in WLP

FO-WLP (many versions)

Silicon Interposer (Data center, networking)

Fan Out Chip on Substrate Package
Growing Number of FO-WLP Applications (Red is Panel Potential)

- Baseband/Application processors
- RF transceivers, switches, etc.
- Power management integrated circuits (PMIC)
- Connectivity modules (IoT)
- Radar modules (77GHz) for automotive
- Audio CODECs
- Microcontrollers
- Logic + memory
  - Data center servers, networking, AI etc. (Fan-out on substrate)
  - Future AP + DRAM for mobile
- Sensors (fingerprint, image, etc.)
- LED
- Many multi-die configurations
Consortia and Companies Working on Panel FO-WLP

- **Companies**
  - SEMCO in Korea
  - Nepes in Korea
  - Powertech Technology (PTI) in Taiwan
  - Unimicron in Taiwan
  - ASE/Deca in Taiwan

- **Consortia**
  - Fraunhofer IZM
  - FOPLP Consortium (ASMPT promoting) in Hong Kong
  - IME A*STAR in Singapore
  - ITRI in Taiwan
  - NCAP in China
  - Jisso Open Innovation of Tops in Japan (New)
  - iNEMI project
Panel-Level FO-WLP Activity

- **PTI is in production with its panel FO-WLP line**
  - Uses chip first process
  - PMIC from MediaTek (1 RDL, 15μm L/S)
- **SEMCO in production with panel FO-WLP for Samsung Gear watch**
  - Application processor and PMIC with backside RDL
  - Backside RDL for memory stacking to form PoP
  - Thin solution
- **NEPES in production with panel FO-WLP for fingerprint sensors**
- **Unimicron continues R&D activity for panel**
- **YOUR NAME HERE**

Images courtesy of Samsung.
Conclusions

- **Economic trends**
  - Some slowing next year
  - Will result in pull back on some expansion
  - Uncertainty created by trade-war, but China expansion continues

- **Smartphones still account for large unit volumes**
  - SiP
  - WLP and FO-WLP

- **DRAM transition from WB to FC**
  - Memory has large unit volumes

- **AI**
  - Watch datacenter server growth
  - AI accelerators demand increasing, drives silicon interposer and HBM

- **Automotive electronics growth, but takes time**
  - Reliability is key

- **WLP growth continues for fan-in and FO-WLP**

- **Panel FO-WLP activity is increasing**
  - Production examples
  - Additional plans
Thank you!

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RELEVANT, ACCURATE, TIMELY
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(continued)

And Many Individuals!
IEEE Frances B. Hugle Memorial Engineering Scholarship (Contributions)

- William Chen in tribute to his wife, Mary Voris Chen
- In memory of Lt. Colonel Arthur S. Metcalfe by his daughter, Kitty Pearsall
- In memory of Adriaan Anton Uijttenbroek, a life member of IEEE by his wife, Janet Clark
- M.S. Lin
Market analysis you can trust!...Multi-client reports

- Flip Chip and WLP Market and Technology Analysis
- New Packages and Materials for Power Devices
- New Frontiers in Automotive Electronic Packaging
- SiP for Mobile and Wearable Applications
- Global Semiconductor Packaging Materials Outlook
  PARTNERSHIP WITH SEMI
- Worldwide OSAT Manufacturing Sites Database
  PARTNERSHIP WITH SEMI
- 3D IC Gap Analysis: Remaining Issues, Solutions, Market Status
- Advanced Packaging Cost Models and Analysis
  PARTNERSHIP WITH SAVANYS

- Advanced Packaging Update (4 issues each year)
  - Economic and business developments in the semiconductor packaging and assembly industry including market forecast by package type (FBGA, FLGA, QFN, stacked die CSP, PoP, etc.)
  - New packages and materials including trends and drivers
  - Various market and technology analysis
At the forefront...Recognizing emerging trends

- **Automotive Electronics**
  First industry analysis of packages for automotive electronics with a focus on ADAS

- **Flip Chip Trends**
  Publishing reports on flip chip markets and technology trends since 1994 — We’ve done cutting edge analysis in recognition of the shift to Cu pillar

- **WLP Demand and Capacity**
  Tracking wafer bump and WLP capacity and demand trends since 2003 — We've been at the front in recognizing the shift to fan-out WLP (FO-WLP)

- **Ball Grid Arrays and Chip Size Packages**
  First industry analysis of the ball grid array market in 1994 — We’ve published annual forecasts of BGA and CSP demand ever since

- **3D IC with TSVs and Si Interposers**
  We are recognized as the provider of realistic market forecasts — We are unique in offering gap analysis, with indications of key areas requiring additional work

- **System-in-Package and Multichip Modules**
  Tracking markets and trends since 1990